

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Parruck, et al.

Assignee: Azanda Network Devices

Title: "Backpressuring Using A Serial Bus Interface And A Status Switch Cell"

Serial No.: Unknown

Filed: October 12, 2001

Examiner: Unknown

Group Art Unit: Unknown

Atty. Doc. No.: AZA-003-7D/2001-P010

J1017 U.S. PTO
09/976206
10/12/01

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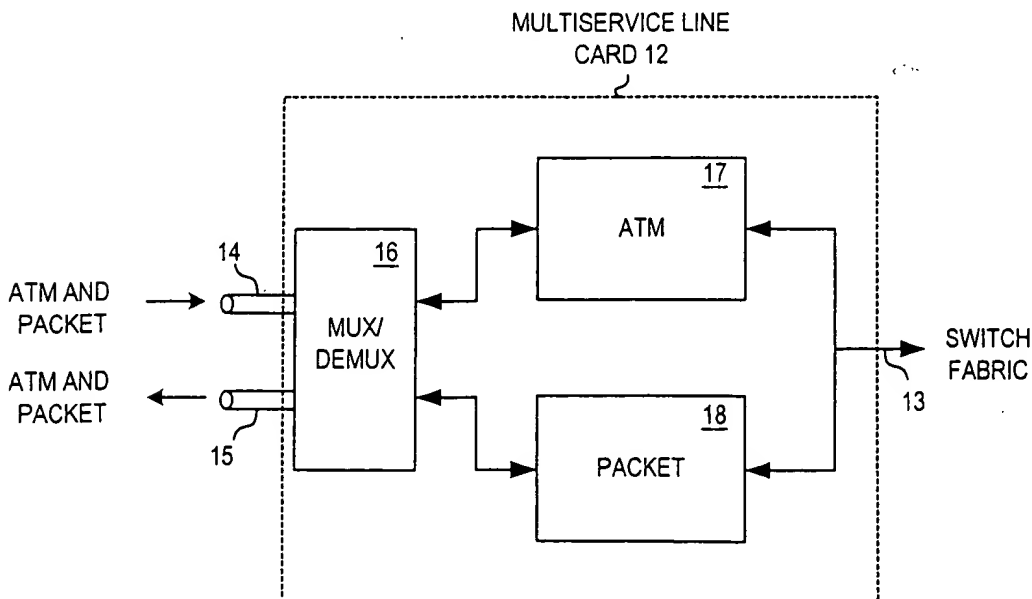
October 12, 2001

Box Patent Application
ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

FIRST INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, Applicants and the undersigned bring the Examiner's attention to the diagram below and certain work that may have taken place or may be taking place at Northern Telecom Ltd. The diagram below is intended to reflect a line card 12 that allegedly was designed by or is being designed by Northern Telecom Ltd.



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Line card 12 is coupled to a switch fabric of a router by interface 13. Cell and packet traffic received from fiber optic cable 14 and transmitted on fiber optic cable 15 are time division multiplexed/demultiplexed by TDM device 16. Cell traffic is handled by one integrated circuit device 17, whereas packet traffic is handled by a different integrated circuit device 18.

The undersigned has spent considerable effort and time attempting to ascertain more details about the line card 12. The undersigned has interviewed individuals at Azanda Network Devices who used to work at Northern Telecom in an attempt to find out more concrete details about the line card, but the individuals interviewed were unable to provide more detail. The details of the line card, even those details illustrated, are not clear in their minds.

In an attempt to be absolutely forthright and candid with the Patent Office, the undersigned is submitting this Information Disclosure Statement. It contains the best information the undersigned has. The Examiner is requested to use the resources at his/her disposal and to search the Patent Office files for any patent applications Northern Telecom may have filed directed to such a line card.

Applicants are unable to state that the line card illustrated is prior art. Consequently, no such admission is being made here. If the Examiner would like to contact the undersigned, the undersigned can be reached at (925) 485-9923.

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" addressed to Box Patent Application, Assistant Commissioner for Patents, Washington, DC 20231, on

October 12, 2001.

Express Mail No. EL928548114US.

T. Lester Wallace

T. Lester Wallace
Signature

Oct. 12, 2001
Date

Respectfully submitted,

T. Lester Wallace

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Attorney for Applicants
Reg. No. 34,748

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11017 U.S. PTO
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10/12/01

Box Patent Application
Assistant Commissioner For Patents
Washington, D.C. 20231

October 12, 2001

SECOND INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 55 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

Copies of the listed documents are not provided because the listed documents were cited in a prior application (09/851,565), the benefit of which is claimed under 35 U.S.C. §120.

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" addressed to Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231, on October 12, 2001, as

Express Mail No. EL928548114US

October 12, 2001
Date

T. Lester Wallace
T. Lester Wallace

Respectfully submitted,

T. Lester Wallace

T. Lester Wallace
Attorney for Applicants
Reg. No. 34,748

U.S. Dept. of Commerce, Patent & Trademark Office	Application No.:
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(Use several sheets if necessary)	Group Art Unit:
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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA	5,666,353	09/09/97	Klausmeier et al.	370	230	03/21/95
	AB	5,742,765	04/21/98	Wong et al.	395	200	06/19/96
	AC	5,787,255	07/28/98	Parlan et al.	395	200	04/12/96
	AD	5,802,052	09/01/98	Venkataraman	370	395	01/26/96
	AE	5,802,287	09/01/98	Rostoker et al.	395	200	08/03/95
	AF	5,809,021	09/15/98	Diaz et al.	370	364	06/10/96
	AG	5,838,915	11/17/98	Klausmeier et al.	395	200	11/17/97
	AH	5,859,835	01/12/99	Varma et al.	370	229	04/15/96
	AI	5,867,480	02/02/99	Thomas et al.	370	230	09/12/96
	AJ	5,870,394	02/09/99	Oprea	370	392	07/23/96
	AK	5,898,688	04/27/99	Norton et al.	370	362	05/24/96
	AL	5,905,725	05/18/99	Sindhu et al.	370	389	04/18/97
	AM	5,917,820	06/29/99	Rekhter	370	392	06/10/96
	AN	5,917,828	06/29/99	Thompson	370	474	01/03/97
	AO	5,920,705	07/06/99	Lyon et al.	395	200	01/30/97
	AP	5,946,313	08/31/99	Allan et al.	370	397	03/20/97
	AQ	5,963,541	10/05/99	Koga et al.	370	229	11/09/95
	AR	5,987,034	11/16/99	Simon et al.	370	465	08/30/96
	AS	6,026,086	02/15/00	Lancelot et al.	370	353	01/08/97
	AT	6,034,945	03/07/00	Hughes et al.	370	230	05/15/96
	AU	6,052,379	04/18/00	Iverson et al.	370	442	09/18/98
	AV	6,088,355	07/11/00	Mills et al.	370	392	10/11/96
	AW	6,091,725	07/18/00	Cheriton et al.	370	392	12/29/95
	AX	6,101,188	08/08/00	Sekine et al.	370	401	09/10/97
	AY	6,111,877	08/29/00	Wilford et al.	370	392	12/31/97

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	BA	6,134,217	10/17/00	Stiliadis et al.	370	232	04/16/96
	BB	6,144,662	11/07/00	Colmant et al.	370	390	07/09/96
	BC	6,147,999	11/14/00	Honda et al.	370	396	05/27/99
	BD	6,154,446	11/28/00	Kadambi et al.	370	239	06/30/99
	BE	6,157,641	12/05/00	Wilford	370	389	08/22/97
	BF	6,160,793	12/12/00	Ghani et al.	370	236	10/13/98
	BG	6,167,027	12/26/00	Aubert et al.	370	230	09/23/97
	BH	6,169,739	01/02/01	Isoyama	370	395	01/08/98
	BI	6,175,570	01/16/01	Cukier et al.	370	414	12/04/96
	BJ	6,185,212	02/06/01	Sakamoto et al.	370	395	04/08/98
	BK	6,188,671	02/13/01	Chase et al.	370	232	12/10/97

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	BL	WO 97/43869	11/20/97	international	H04Q			
	BM	WO 99/17182	04/08/99	international	G06F			
	BN	WO 00/56012	09/21/00	international	H04L			

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		Atty. Docket: AZA-003-7D/2001-P010
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
CA	Application 09/528,802, filed 03/20/00, "Circuits for Combining ATM and Packet Data on an Optical Fiber and Methods Therefor," by Parruck, Nguyen, Ramakrishnan	
CB	Application 09/539,479, filed 03/30/00, "Devices for Aggregating ATM Cells and Packets for Transmission Over the Same Channel," by Parruck, Nguyen, Ramakrishnan	
CC	Application 09/539,306, filed 03/30/00, "Devices for Employing SONET Frames for Transporting both ATM Cells and Packets Over in the Same Channel in an Optical Fiber," by Parruck, Nguyen, Ramakrishnan	
CD	Application 09/539,478, filed 03/30/00, "Methods and Apparatus for Dynamically Allocating Bandwidth Between ATM Cells and Packets," by Parruck, Nguyen, Ramakrishnan	
CE	Application 09/539,461, filed 03/30/00, "Routers for Switching ATM Cells in a Packet-like manner Using a Packet Switch," by Parruck, Nguyen, Ramakrishnan	
CF	Application 09/539,476, filed 03/30/00, "Processor-based Architecture for Facilitating Integrated Data Transfer between both ATM and Packet Traffic with a Packet Bus or Packet Link, Including Bidirectional ATM-to-Packet Functionality for ATM Traffic," by Parruck, Nguyen, Ramakrishnan	
CG	Application 09/539,477, filed 03/30/00, "Integrated ATM/Packet Segmentation-and-Reassembly Engine for Handling both Packet and ATM Input Data and for Outputting both ATM and Packet Data," by Parruck, Nguyen, Ramakrishnan	
CH	"Is the Bloom Off Jasmine" and other pages from 'Light READING' web site "www.lightreading.com", 2 pages (printed 03/26/01).	
CI	Chapter 3, Frame Relay Architecture, from "Frame Relay Technology and Practice" textbook, by Jeff T. Buckwalter, (copyright 2000).	
CJ	Chapter 5, MPLS Core Protocols, from "MPLS Technology and Applications" textbook, by Bruce Davie and Yakov Rekhter, (copyright 2000).	
CK	"GenFlow Family of Traffic Management Co-Processors" and other pages from Acorn Networks' web site "www.Acorn-Networks.com", 5 pages (printed 3/30/01).	
CL	White Paper entitled "Wire-Speed Traffic Management Solutions Based On Coprocessors", by Joe Adler and Tam-Anh Chu, Acorn Networks, 14 pages (copyright 2000).	
CM	S19202 Advanced Summary Datasheet for "Ganges STS-192 POS/ATM Sonet/SDH Mapper", Applied Micro Circuits Corporation, revision 1.6, 2 pages (June 2000).	
CN	"Product Guide - Ganges (S19202)" and other pages from Applied Micro Circuits Corporation's web site, 3 pages (printed 2/27/01).	
CO	"ZettaCom - Powering Tomorrow's Network", pages from ZettaCom's web site "www.zettacom.com/products", 2 pages (downloaded 1/31/01).	
CP	"Ocular Networks' Metro Business Access Architecture, pages from Ocular Networks' web site "www.ocularnetworks.com/architecture", two pages (downloaded 1/31/01).	
Examiner		Date Considered
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